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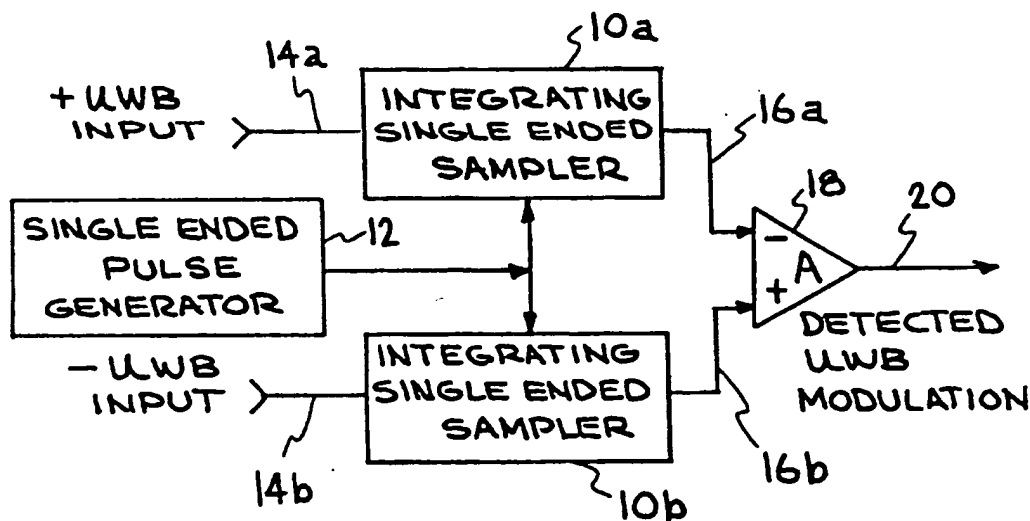
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INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

(51) International Patent Classification ⁵ : H04K 1/00	A1	(11) International Publication Number: WO 94/24788
		(43) International Publication Date: 27 October 1994 (27.10.94)
(21) International Application Number: PCT/US94/03893		(81) Designated States: CA, JP, KR, European patent (AT, BE, CH, DE, DK, ES, FR, GB, GR, IE, IT, LU, MC, NL, PT, SE).
(22) International Filing Date: 8 April 1994 (08.04.94)		
(30) Priority Data: 08/044,745 12 April 1993 (12.04.93) US		
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(54) Title: ULTRA-WIDEBAND RECEIVER



(57) Abstract

An ultrawide (UWB) receiver utilizes a strobed input line (14) with a sampler (10) connected to an amplifier (18). In a differential configuration (18), +UWB inputs (14a) are connected to separate antennas or to two halves of a dipole antenna. The two input lines (14) include samplers (10) which are commonly strobed (12) by a gating pulse with a very low duty cycle. In a single ended configuration, only a single strobe input line and sampler is utilized. The samples integrate, or average, up to 10,000 pulses to achieve high sensitivity and good rejection of uncorrelated signals.

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ULTRA-WIDEBAND RECEIVER

The United States Government has rights in this invention pursuant to Contract No. W-7405-ENG-48 between the United States Department of Energy and the University of California for the operation of Lawrence Livermore National Laboratory.

BACKGROUND OF THE INVENTION

The invention relates generally to electrical signal transmission and reception, and more particularly to ultra-wideband receivers.

Direct radiation of an electrical impulse without a carrier frequency is a form of ultra-wideband (UWB) transmission which has recently received much attention for high resolution radar and spread-spectrum communications. Since an impulse contains a broad spectrum of frequencies, radars and radios that use impulsive transmitters are inherently spread-spectrum and thus present the possibility of dramatically increasing the usage of crowded airwaves through time domain channel selection rather than frequency domain channel selection. Impulse systems also promise simplified hardware implementation.

At present, a primary limitation to the development and commercialization of UWB systems has been the lack of suitable hardware. Although UWB impulse sources and antennas have received considerable recent attention, UWB receivers remain virtually undeveloped.

Prior art receivers have exclusively consisted of commercial transient digitizers and repetitive sampling oscilloscopes, both of which suffer from high costs (\$30K-\$100K), poor sensitivity (several millivolts), low repetition rates (10Hz - 50kHz), and large bulk.

SUMMARY OF THE INVENTION

Accordingly it is an object of the invention to provide a UWB receiver.

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It is also an object of the invention to provide a simple, compact, low cost, highly sensitive UWB receiver.

It is another object of the invention to provide a high repetition rate, low peak power UWB receiver.

The invention is a UWB receiver which has a preferred differential embodiment, and an alternate single ended embodiment. Either embodiment may be constructed to operate in a voltage mode or a current mode. In the differential embodiment, positive and negative inputs are commonly strobed into a differencing amplifier. The strobe pulse width is generally a very small fraction of the strobe pulse repetition interval. The outputs of two integrating single-ended samplers driven by a common single-ended pulse generator are input into a differencing amplifier. In a single ended embodiment, only a single strobed input is applied to an amplifier. The samplers integrate, or average, up to 10,000 pulses to achieve high sensitivity and good rejection of uncorrelated signals.

This invention is a receiver specifically designed for the reception of UWB signals. It is simple, compact, low cost (~\$10), and highly sensitive (~1 microvolt). Further, it operates at high repetition rates (>10MHz) and has excellent system noise rejection.

An additional feature of the UWB receiver is that it detects the difference in signals between two or more antennas, thus simplifying systems using antenna arrays. This feature may also be used in microwave directional detectors for network analyzers.

This invention is primarily intended for high repetition rate, low peak power UWB systems. High repetition rate systems can be used to generate substantial average transmit power levels while keeping peak power levels at a low, practical level. When combined with a random, or dithered, Pulse Repetition Interval (PRI), the UWB emission spectrum can be spread to the point where it resembles thermal noise to aid in interference reduction. The high sensitivity of this invention permits average transmit power to be reduced to less than one microwatt for many consumer applications, a power level that will assure FCC, OSHA, and EPA approval.

BRIEF DESCRIPTION OF THE DRAWINGS

Figures 1a-d illustrate four basic UWB receivers.

Figure 2a is a block diagram of a differential UWB receiver.

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Figure 2b is a schematic diagram of a differential UWB receiver.

Figure 3a shows a simulated integrated detection voltage waveform at the junction of C1 and D1 in Figure 2.

Figure 3b is a magnified view of the waveform of Figure 3a.

Figure 3c shows the current through D1.

Figure 4 shows the detection voltage response at the C1, D1 junction for a 5mV input step.

Figure 5 is a schematic diagram of a prior art sampler.

Figure 6 illustrates rejection of bias voltage variations vs. frequency.

Figure 7 is a schematic diagram of the connection of more than two antennas.

Figure 8 is a schematic diagram of a single ended UWB receiver.

Figures 9 a,b are schematic diagrams of voltage and current mode configurations of a single operational amplifier receiver/alarm.

DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENTS

Four basic ultra-wideband (UWB) receivers are illustrated in Figures 1a-d. In the voltage mode differential configuration of Figure 1a, negative and positive input terminals are connected through a series capacitor and resistor C1-R1, C2-R2 to the positive and negative inputs of differencing amplifier A. The input channels are commonly strobed through diodes D1, D2. The strobe line may include a capacitor Cs for pulse shaping, if required, and is terminated in a resistor RT. C1 and C2 act as charge holding capacitors. R1 and R2 isolate the strobe pulse from appearing at the amplifier input terminals, and from being shunted to ground by C3, C4. A parallel resistor and capacitor R3-C3, R4-C4 is also connected from each amplifier input to ground and act as integrators for voltage developed across C1, C2. In an alternate current mode differential receiver of Figure 1b, the positive and negative input terminals are connected through C1-R1, C2-R2 to the difference amplifier A3 through additional operational amplifiers A1, A2 configured as differential transimpedance amplifiers. Current mode is preferred for diodes with higher leakage currents or for high sensitivity systems. In the voltage mode, a voltage builds up at C1, C2. In the current mode, charge on C1, C2 is immediately bled off by the

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transimpedance amplifiers and accumulated on integrating capacitors in the feedback loop. The single ended voltage and current mode receivers of Figures 1c,d have only a single strobed input line connected to an amplifier A. A single input terminal is connected through C1-R1 to the positive input of A in Figure 1c, and to the negative input of A in Figure 1d. The other input of A is connected to ground as a reference. Thus a differencing amplifier is not needed for the SE configurations since ground can be used as a reference for the amplifiers.

A block diagram of a differential UWB receiver of the invention in Figure 2a shows two integrating single-ended (SE) samplers 10a,b driven by a common, SE pulse generator 12. Single-ended means single input and output lines 14a,b and 16a,b, as opposed to differential, or balanced structures that use two lines to carry the signal or pulse waveforms in mirror image fashion. The outputs of the SE samplers 10a,b are combined in the differencing amplifier (A) 18 to obtain a single output 20 that represents the detected difference between the +UWB input and the -UWB input. Pulse generator 12 provides a gating pulse to samplers 10a,b. The gating pulse typically has a width which is much shorter than the pulse repetition interval (PRI), e.g. 10^{-3} to 10^{-6} of the PRI. Typical gating pulse widths are from about 5 ps to about 5 ns and PRI of about 100 ns to about 100 μ s. A number of pulses are averaged, or integrated, by the samplers. The typical number of pulses integrated range from 2 to 10,000 depending on the application.

This structure performs the differencing operation at IF (intermediate frequencies) or at baseband. The frequency of the differencing operation is determined by the values of the circuit components, i.e. capacitors and resistors. Baseband refers to frequencies equal to the modulation, or intelligence carrying frequencies. For many UWB systems, the UWB input to the receiver may span several gigaHertz, while the baseband output may span only a few kiloHertz.

It is far easier to perform the differencing operation at baseband than at UWB frequencies. The differencing operation compares two UWB signals from two separate antennas, or it combines the input from the two halves of a dipole antenna, eliminating the need for a wideband balun transformer. Perhaps more importantly, common voltages produced by the SE samplers, such as bias voltages, are subtracted out.

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A schematic of a preferred embodiment is shown in Figure 2b. The +UWB and pulse generator (strobe) inputs are brought to the circuit via transmission lines 22a,b and 24 which are terminated by resistors R_T which are equal to the characteristic impedance of the lines.

The +UWB input is connected through line 22a, capacitor C1 and resistor R1 to the positive input of a first operational amplifier (A1) 26a. The -UWB input is connected through line 22b, capacitor C2 and resistor R2 to the positive input of a second operational amplifier (A2) 26b.

Capacitors C1 and C2 are of equal value and form signal integrators, i.e., the time constant $R_T \cdot C1$ is substantially larger than the sampling gate width provided by the pulse generator, and the discharge times of C1, C2 are larger than the pulse repetition interval (PRI) of the pulse generator. The discharge time of C1, C2 is set by the current provided through R3, R4 connected from the positive inputs of amplifiers A1, A2 to a +Bias voltage. R3, R4 have typical values in the megohm range and C1, C2 may typically be 0.01 microfarad, for pulse generator pulse widths of 0.1ns and a PRI of 1 microsecond.

Resistors R1, R2 isolate the UWB frequencies from amplifiers A1, A2. Their value must be high compared to R_T , but not so high that noise performance is degraded. A typical value is 10K ohms. The product of R1, R2 times the input capacitance of A1, A2 must be high compared to the pulse generator pulse width.

Additionally, capacitors C3, C4 may be placed from the plus inputs of A1, A2 to ground to improve isolation, and to prevent radio frequencies from the UWB inputs from appearing at the amplifiers inputs, which could result in erratic performance. Only detected baseband voltages should appear at the amplifier inputs.

The output of A1 is connected through series C5 and R5 to the negative input of a third operational amplifier (A3) 28. The output of A2 is connected to the positive input of A3. The output of A3 is connected back to its negative input through parallel C6, R6. The outputs of A1, A2 are connected to their negative inputs through parallel combination C7, R7. The negative inputs of A1, A2 are also connected together through series C8 and R8. Amplifiers A1, A2, A3 form a standard high input impedance fully differential amplifier, i.e. they form the differential amplifier (A) 18 of Figure 2a. In this

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embodiment, A1, A2, and A3 are J-FET input operational amplifiers contained on a single chip, part number TLO-74 by Texas Instruments. C5-R5 and C6-R6 function as differentiators and integrators, respectively, or in combination, as a baseband bandpass filter.

A pair of diodes D1, D2 are connected in series, cathode to cathode, from the junction between C1 and R1 to the junction between C2 and R2. Pulse generator input transmission line 24 is connected to the junction between D1 and D2.

The polarity of diodes D1, D2 and the polarity of the associated bias (+Bias) and gate pulse in Figure 2b may be reversed with no essential change in performance. Also, D1 and C1 may be interchanged as well as D2, C2 with essentially the same performance.

As a receiver, this invention provides high sensitivity, high gain, and high selectivity. High sensitivity is achieved through a high level of signal integration and high detection efficiency. High sensitivity is only practical when extraneous noise sources can be rejected through pulse integration.

High gain is provided by the baseband differential amplifier, which in many cases may consist of low cost operational amplifiers.

High selectivity is attained through high levels of time coherent, repetitive signal sampling and averaging. Further, when combined with PRI dithering or pseudorandom PRI modulation, extraneous signals are more completely "averaged out". For this to work, a UWB receiver must not produce extraneous outputs when the PRI is modulated. Prior art samplers, such as commercially available sampling oscilloscopes, exhibit low tolerance to PRI modulation, i.e. they exhibit jitter; further, they cannot operate at PRI's shorter than 20 microseconds. In this invention, the minimum PRI is limited only by the pulse generator, and PRI variations are rejected by the common-mode rejection of the differential amplifier.

Figure 3a shows a numerical simulation of the integrated detection voltage waveform appearing at the junction of C1 and D1 with a PRI of 0.1 microseconds and a pulse width of 0.2ns. There is an average bias level of 100mV, whereas this level would be ~500mV without the application of the pulse generator. Thus the average rectified gate pulse generator signal is -400mV. Figure 3b shows the

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waveform of Figure 3a magnified about the average bias level, where the voltage on C1 is seen to ramp up between pulse generator spikes. This is due to the bias current flowing through R3 in Figure 2b.

Figure 3c shows a time-scale magnification of the current through D1 for a pulse width of 0.2ns and PRI of 0.1 microsec. The diode current is seen to flow for ~0.2ns, the duration of which is related to the charge and discharge rates of C1 and the applied pulse width.

The waveform in Figure 3a approximates the integrated average of the difference between the gate pulse generator input and the signal input. Further averaging occurs in the differential amplifier, or in a low pass filter placed after the differential amplifier, and results in a smooth level when no UWB input is applied.

Figure 4 is a magnification of the voltage appearing at the junction of C1, D1, similar to Figure 3a. At time $t=0$, the average voltage is 106mV and a +5mV step is applied to the +UWB input. The average value changes -5mV. The minus sign results because after the gate pulse passes, the UWB input pulse returns from its +5mV level to 0.0 V, driving the voltage at C1, D1 negative. This detector has a voltage magnitude transfer efficiency of approximately 100% from UWB input to detected output.

Prior art samplers have a much lower voltage transfer efficiency. Figure 5 shows a prior art sampler driven by a balanced pair of strobe transmission lines T1, T2. When all four diodes are driven into conduction by pulses on T1, T2, the shunting effect of the impedance of T1, T2 combined with source impedance Z and diode resistances R_d , result in voltage division in the transfer of the input voltage to charge holding capacitor CHold. The net efficiency may be ~25%. Since post-detection noise dominates the total noise, low detection efficiency means a poor signal to noise ratio on weak signals.

By symmetry, voltages seen at the junction of C1, D1 in Figure 2b are also seen at the junction of C2, D2 when there is no UWB input. If these voltages are equal, they are differenced to zero by the differential amplifier. Thus, variations in detected voltage caused by PRI variations, gate pulse amplitude or width variations, and bias supply variations are rejected.

Figure 6 indicates bias voltage rejection vs frequency for a 1% mismatch in the values of C1, C2. If all components were perfectly

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matched, rejection would be limited by the common mode rejection ratio of the differential amplifier, typically 100dB. In practice, 40dB of rejection for the complete circuit is typical. In contrast, the best broadband baluns, such as may be found in prior art samplers, as shown in Figure 5 for example, may result in only a 20dB rejection of gate pulse variations. Rejection of bias and gate pulse variations is extremely important when detecting UWB input pulses on the order of one microvolt. With only 20dB of rejection, a 10 microvolt bias variation would appear as a one microvolt UWB signal input.

The topology of this invention lends itself to simplified signal combining while retaining the advantages of differential operation. This feature will find use in antenna array systems for synthetic beam forming and imaging radars.

Since each input, +UWB, -UWB, may be connected to an antenna feed line, the differential configuration is ideal for combining the two halves of a single dipole antenna. A common UWB antenna is the tapered slot antenna, which has two halves which generally must be connected to a coaxial transmission line via a wideband balun transformer to maintain balance and thus avoid feed line radiation or pickup. This invention eliminates the need for the wideband balun by connecting the +UWB, -UWB inputs to the two halves through two coaxial cables, or ideally, by co-locating the antenna and UWB receiver and replacing the coaxial cables with a direct connection of the two dipole halves to the two receiver inputs. Baluns are currently impossible to realize at the wide bandwidths and high levels of balance provided by this invention.

Figure 7 shows a simple configuration to connect more than two antennas using additional SE detectors driven from a common pulse generator. Three channels 30a,b,c are shown where channel 30a may be a reference channel (REF) or UWB CH0 and channels 30b,c are UWB CH1 and CH2. Each channel is connected to a respective antenna 32a,b,c (ANT. 0,1,2). Each channel has an input transmission line 34a,b,c terminated at a resistor RT. Each input line 34a,b,c is connected through a series capacitor C1 and resistor R1 to the positive input of an operational amplifier 36a,b,c. The positive inputs of amplifiers 36a,b,c are also connected to a +Bias voltage through a resistor R2. The junction between C1 and R1 on each channel is connected through a

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diode D1 to gate pulse input line 38. The output of each amplifier 36a,b,c is connected back to the negative input of the amplifier through a resistor R3. The negative input of amplifier 36a is also connected to baseband summation line 40, while the negative inputs of amplifier 36b,c are connected to line 40 through an impedance, represented by series resistor R4 and capacitor C2. In some cases, capacitor C2 can be omitted. Although shown with three channels, additional channels can similarly be added. Tests and numerical simulations indicate that a 50 ohm gate pulse generator of several volts in amplitude can drive ~10 SE detectors. With more amplitude, more detectors can be driven. This technique provides as outputs sums and differences of various UWB antenna inputs. It simplifies multiple embodiments of Figure 2 while retaining high rejection of bias and gate pulse variations.

The -UWB input and associated circuitry shown in Figure 2b may be deleted to form a SE detector as shown in Figure 8. The circuitry is identical to Figure 2b except that the connections for amplifiers 26a and 28 to amplifier 26b are replaced by connections to ground. General SE voltage and current mode configurations are shown in Figures 1c,d. This configuration is usable in non-dithered, high signal level applications. A primary limitation is that variations in gate pulse generator width, amplitude, and PRI appear superimposed on the detected UWB signal output. Nevertheless, the SE configuration may be applied to very low cost sensors, such as short range UWB radar hidden object locators.

As shown in Figure 9a, a receiver/alarm circuit is formed of a single operation amplifier, e.g. TLC 27L2, connected to a 15" wire antenna. A gating pulse is applied from a 74HC04 CMOS inverter through diode 1N5711 to the input channel. A typical gate pulse is 1V, 2ns wide. The corresponding current mode circuit is shown in Figure 9b.

This invention will find widespread use in impulse radar. Particular applications include security alarms and automotive collision sensing of range, motion and velocity; robotics control and fluid level sensing; medical imaging, subsurface imaging, non-destruction evaluation (NDE) and security imaging.

UWB motion sensors based on this invention are described in copending U.S. Patent Application Serial No. (IL-9092, RL-12,055,

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S-77,732) entitled "Ultra-Wideband Radar Motion Sensors" filed herewith.

This invention makes an excellent spread spectrum communications receiver. Its high detection efficiency results in a low noise figure, unlike prior art samplers, and it rejects aberrations caused by modulated PRI, an essential characteristic of impulsive spread-spectrum systems. One example of an impulsive spread-spectrum system would be a cordless telephone with direct digital, or time-domain, channel selection of millions of channels, thus opening more spectrum usage while at the same time making eavesdropping nearly impossible.

Since this invention does not require transformers or baluns, it is suitable for monolithic integration. This invention will help pave the way for single-chip, low cost impulse radars and spread-spectrum communications.

Changes and modifications in the specifically described embodiments can be carried out without departing from the scope of the invention which is intended to be limited only by the scope of the appended claims.

CLAIMS

1. An ultra-wideband (UWB) receiver, comprising:
a differencing amplifier;
at least one input channel connected to one input of the amplifier;
strobing means connected to each input channel.
2. The UWB receiver of Claim 1, further comprising:
a second input channel connected to the other input of the amplifier,
wherein the strobing means are connected to both input channels to
simultaneously strobe both channels.
3. The UWB receiver of Claim 1, wherein each input
channel includes an integrating capacitor and a frequency isolating
resistor connected in series.
4. The UWB receiver of Claim 2, wherein each input
channel includes an integrating capacitor and a frequency isolating
resistor connected in series.
5. The UWB receiver of Claim 1, wherein the strobing
means is connected to each input channel through a diode.
6. The UWB receiver of Claim 1, further comprising a
second resistor and second capacitor connected in parallel to ground
from each amplifier input having an input channel connected thereto.
7. The UWB receiver of Claim 2, further comprising a
differential transimpedance amplifier in each of the channels.

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8. The UWB receiver of Claim 4, further comprising a differential transimpedance amplifier in each of the channels between the series integrating capacitor and frequency isolating resistor and the input to the differencing amplifier.

9. The UWB receiver of Claim 1, comprising a single input channel, wherein the other input of the amplifier is connected to ground.

10. A differential ultra-wideband (UWB) receiver, comprising:

- a positive UWB input line,
- a first integrating single ended sampler connected to the positive UWB input line;
- a negative UWB input line;
- a second integrating single ended sampler connected to the negative UWB input line;
- a differential amplifier having its negative input connected to the output of the first sampler and its positive input connected to the output of the second sampler;
- a single ended gate pulse generator connected to both samplers to provide a common gating pulse.

11. The UWB receiver of Claim 10, wherein the gate pulse generator is connected to each input line through a diode.

12. The UWB receiver of Claim 10, wherein the gate pulse generator produces a gating pulse having a pulse width of about 5ps to 5ns and a pulse repetition interval of about 100ns to 100μs.

13. The UWB receiver of Claim 10, further comprising a dipole antenna connected to the positive and negative UWB input lines.

14. The UWB receiver of Claim 10, wherein the first and second samplers operate at intermediate (IF) or baseband frequencies.

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15. The UWB receiver of Claim 10, wherein each sampler comprises:

- an integrating capacitor;
- a frequency isolating resistor;
- the capacitor and resistor being connected in series to the positive input of the differential amplifier.

16. The UWB receiver of Claim 15, wherein the gate pulse generator is connected to the junction between the integrating capacitor and isolating resistor.

17. The UWB receiver of Claim 16, further comprising a diode connected between the gate pulse generator and the junction.

18. The UWB receiver of Claim 17, wherein each sampler further comprises:

- a resistor connected from the positive input of the differential amplifier to a bias voltage;
- an isolation capacitor connected from the positive input of the differential amplifier to ground.

19. A multi-input UWB receiver, comprising:

- two or more input lines;
- a plurality of differencing amplifiers, one connected to each input line at its positive input;
- a plurality of antennas, one connected to the input of each input line;
- a gate pulse line connected to each input line to simultaneously strobe each input line;
- a baseband summation line, connected directly to the negative input of one of the amplifiers and to the negative input of the other amplifiers through an impedance.

20. The receiver of Claim 19, wherein each input line comprises a series capacitor and resistor, wherein the gate pulse line is connected to the junction therebetween through a diode, and a parallel

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resistor is connected from the positive input of the amplifier to a bias voltage.

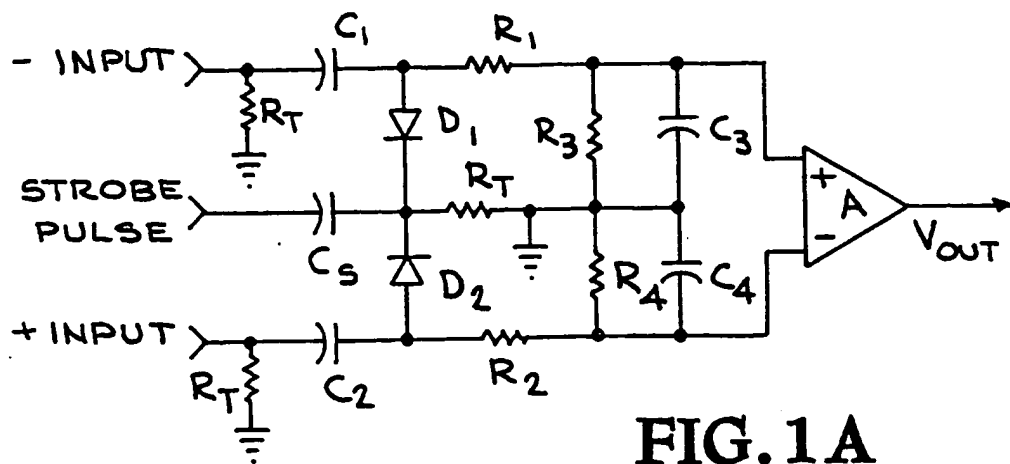


FIG. 1A

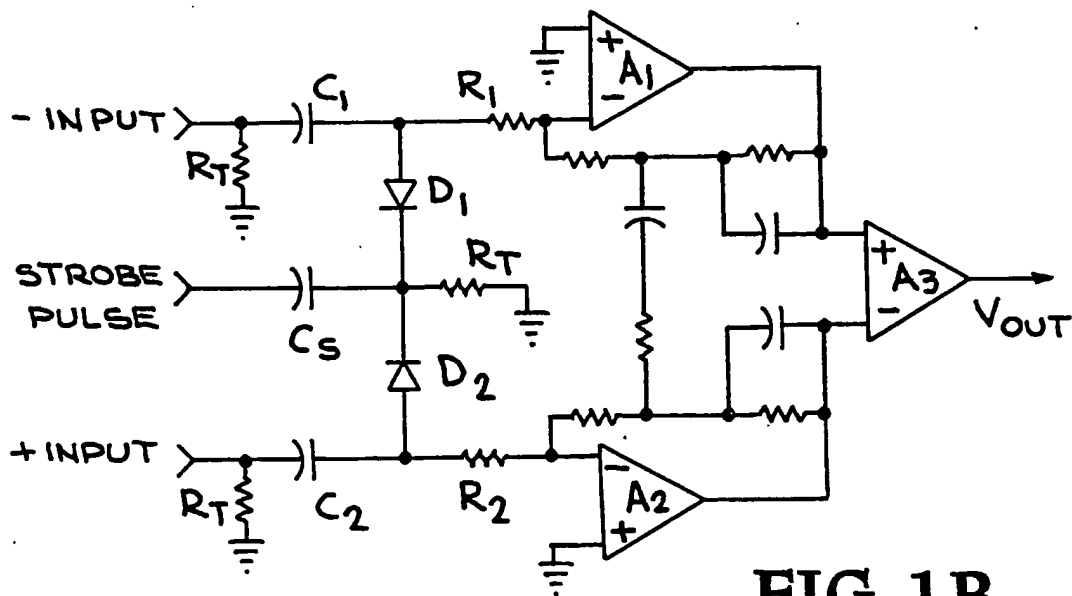


FIG. 1B

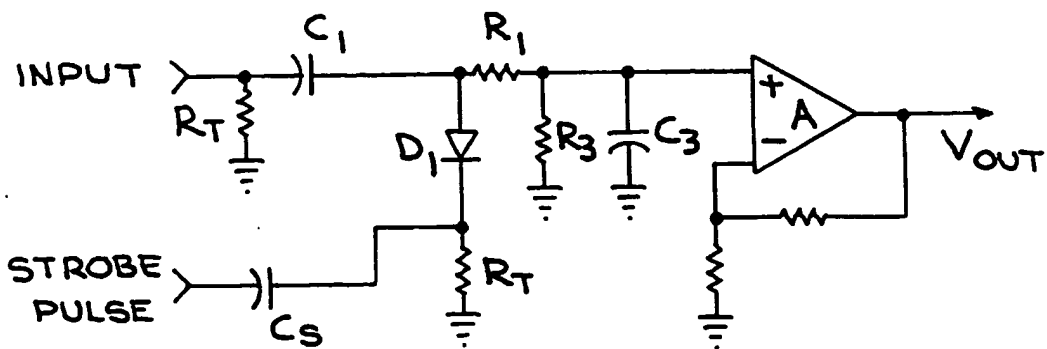
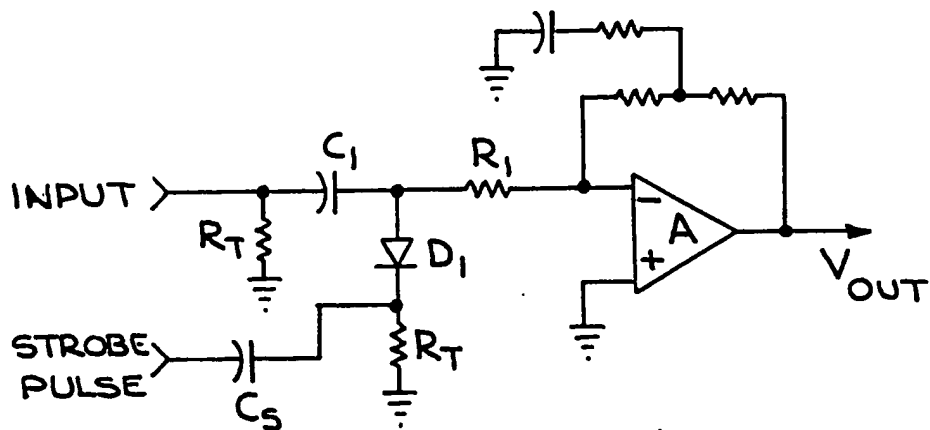
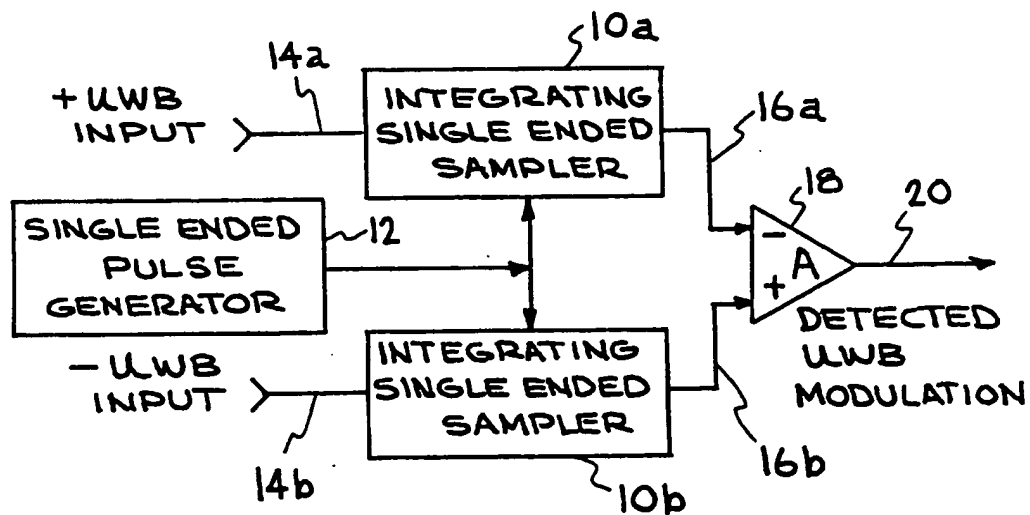


FIG. 1C

**FIG. 1 D****FIG. 2A**

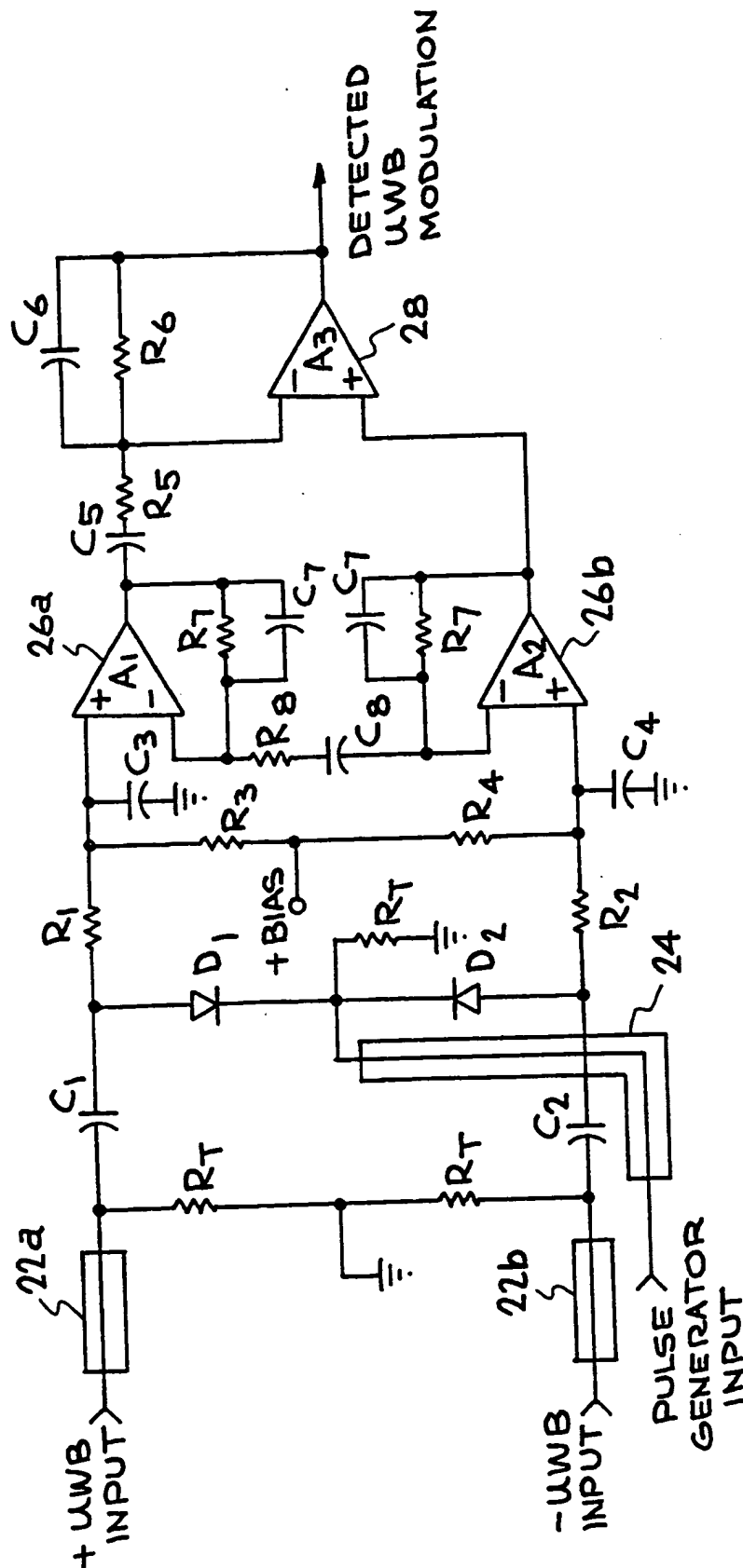
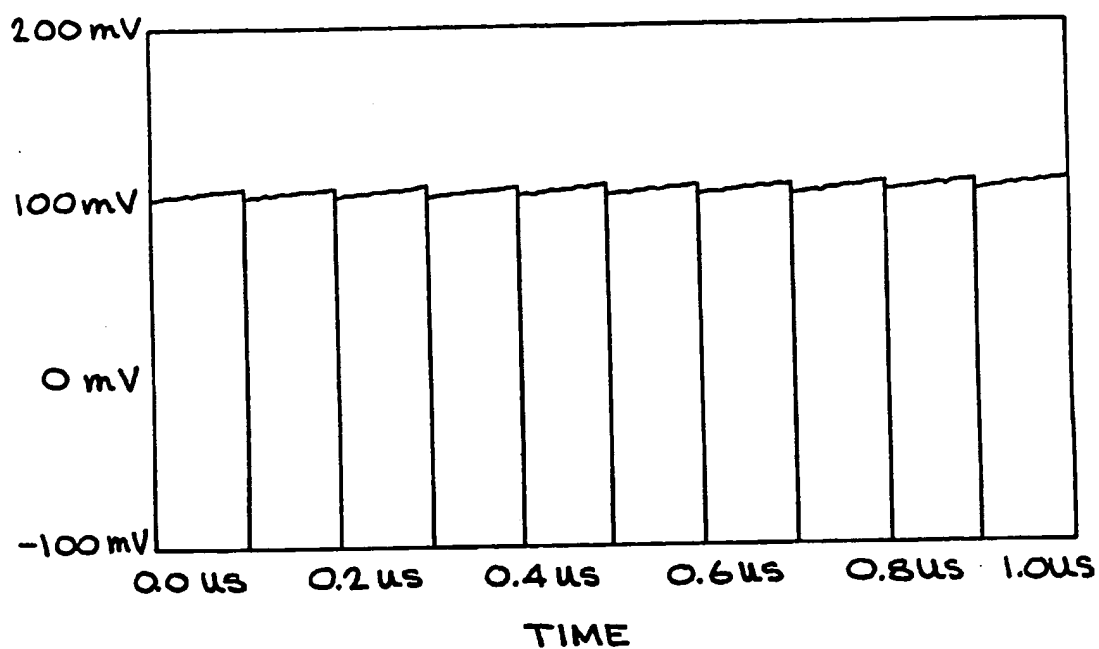
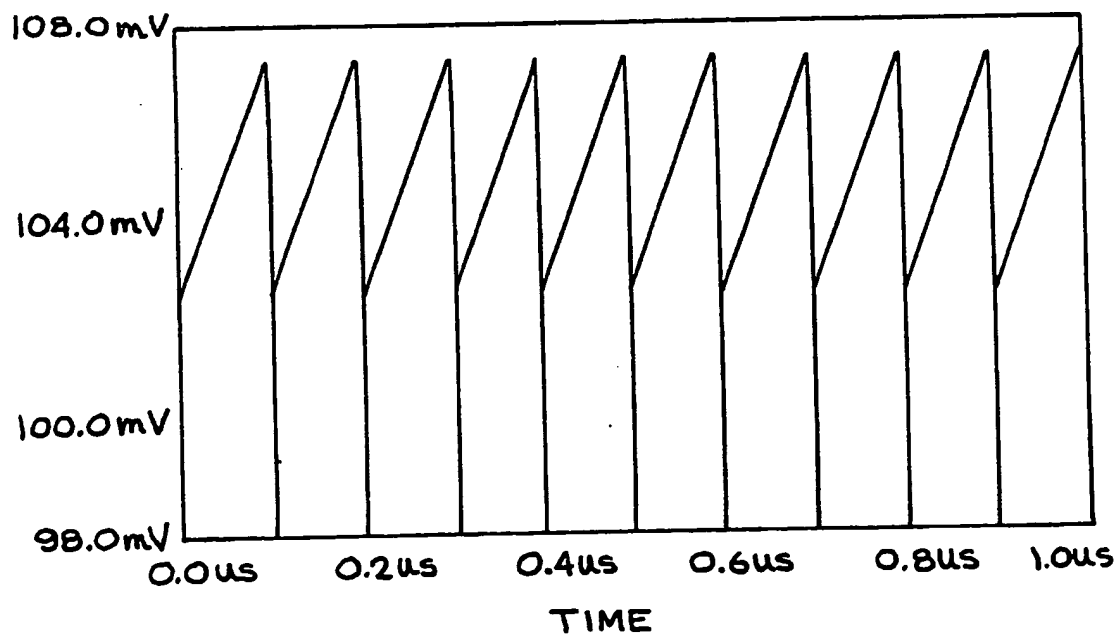


FIG. 2B

**FIG. 3A****FIG. 3B**

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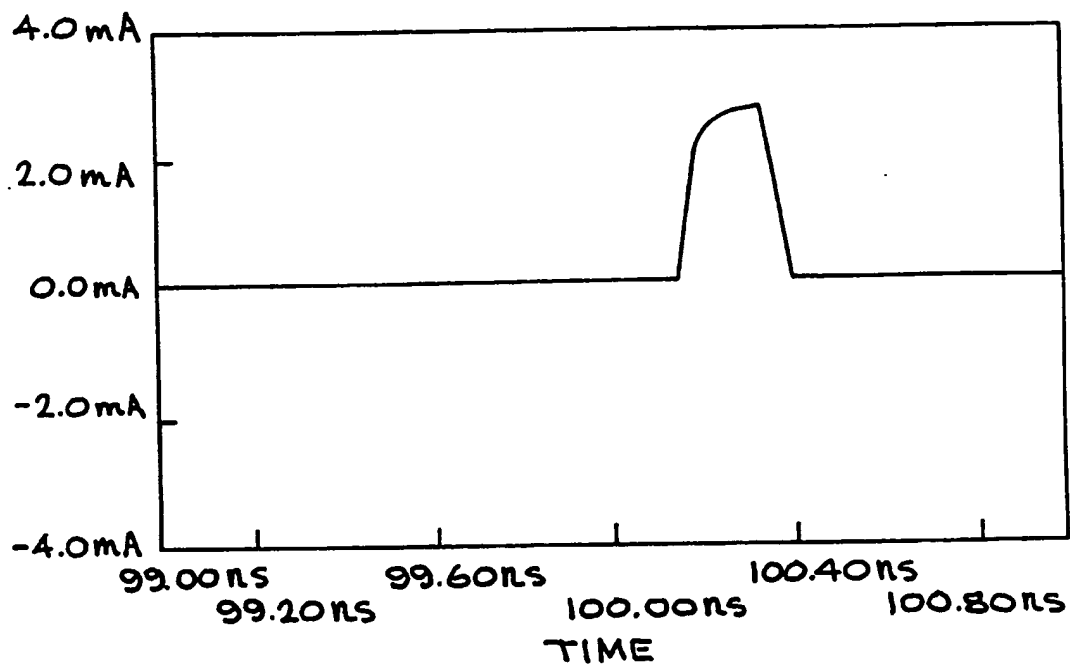


FIG. 3C

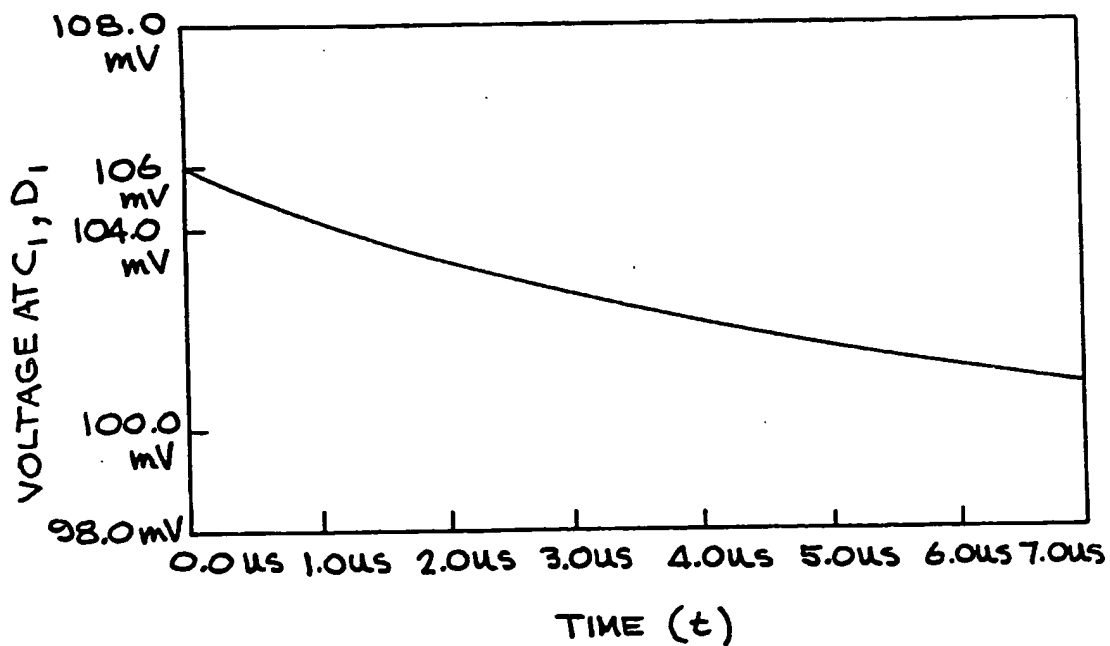


FIG. 4

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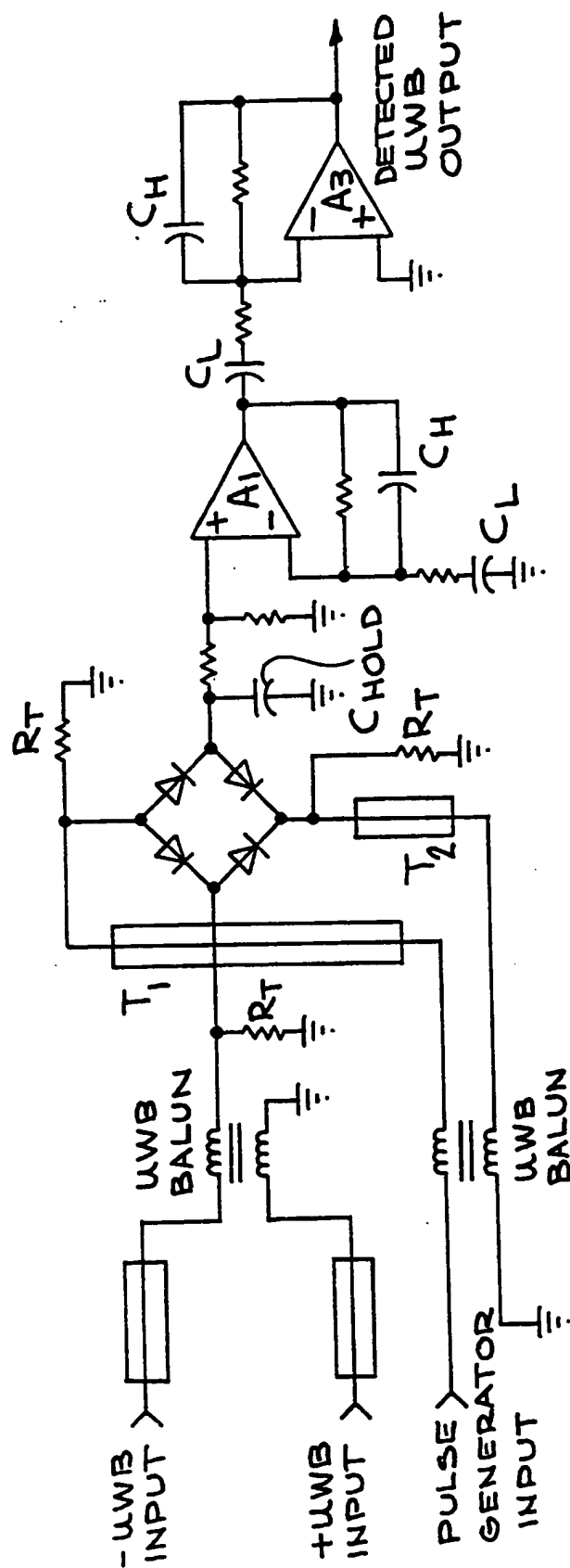


FIG. 5 (PRIOR ART)

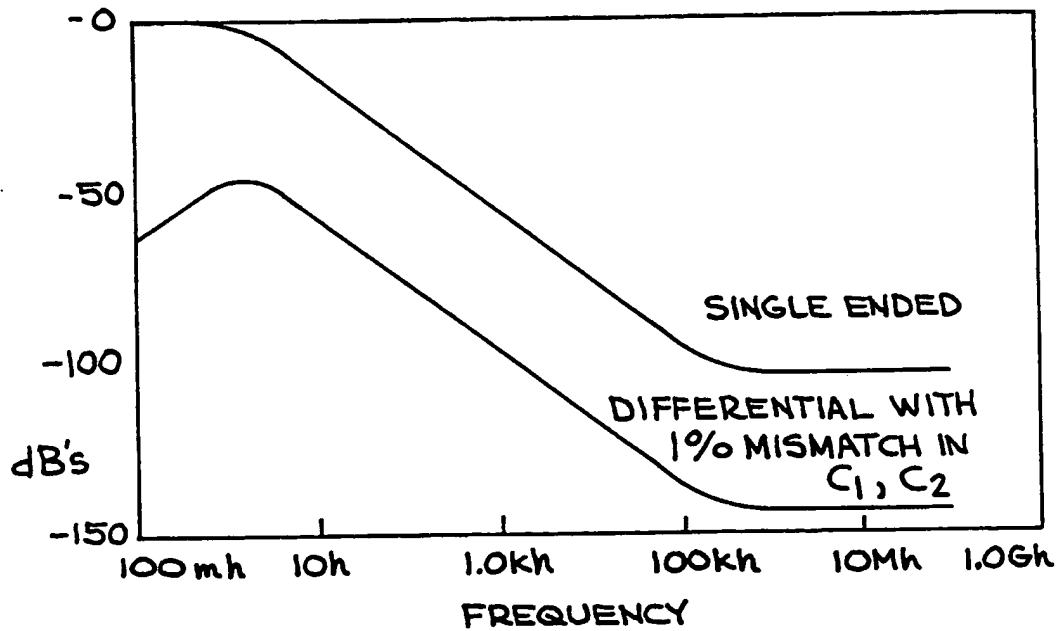


FIG. 6

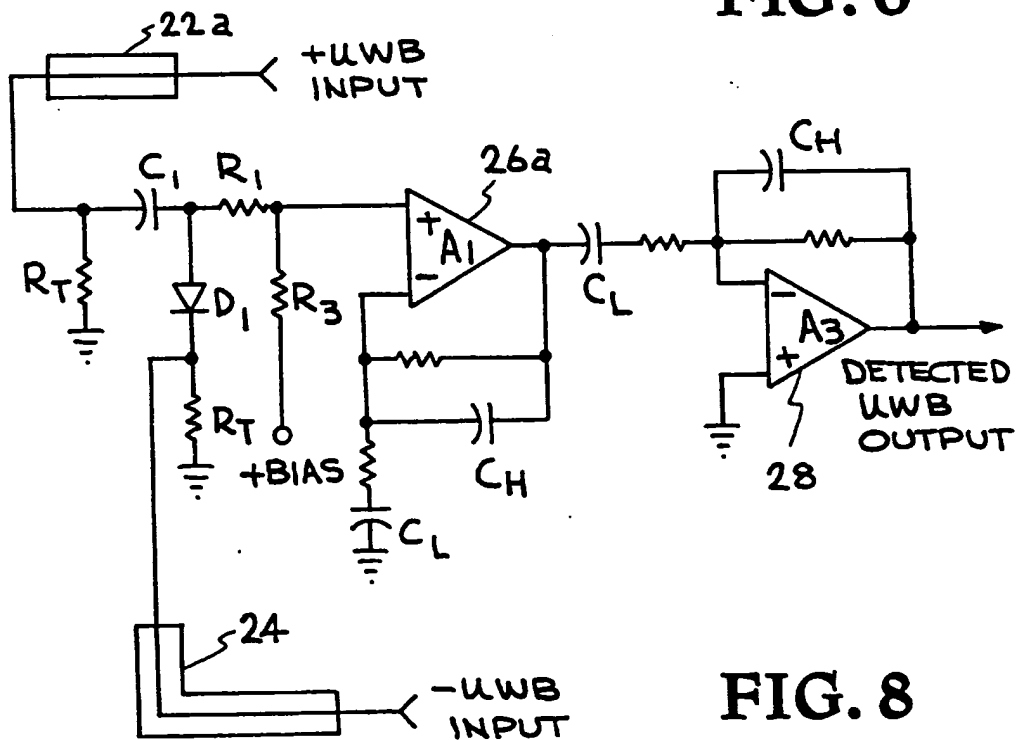


FIG. 8

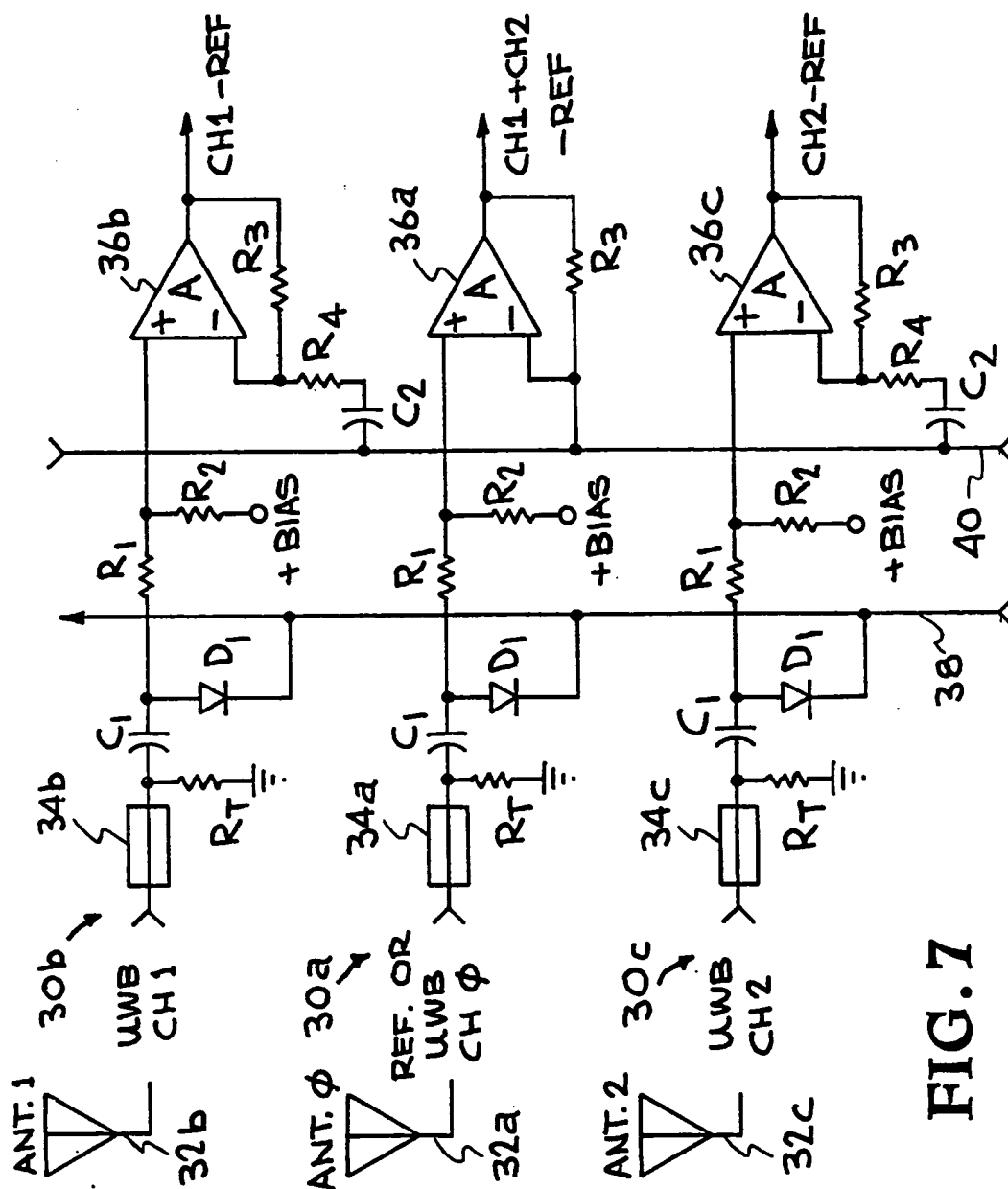
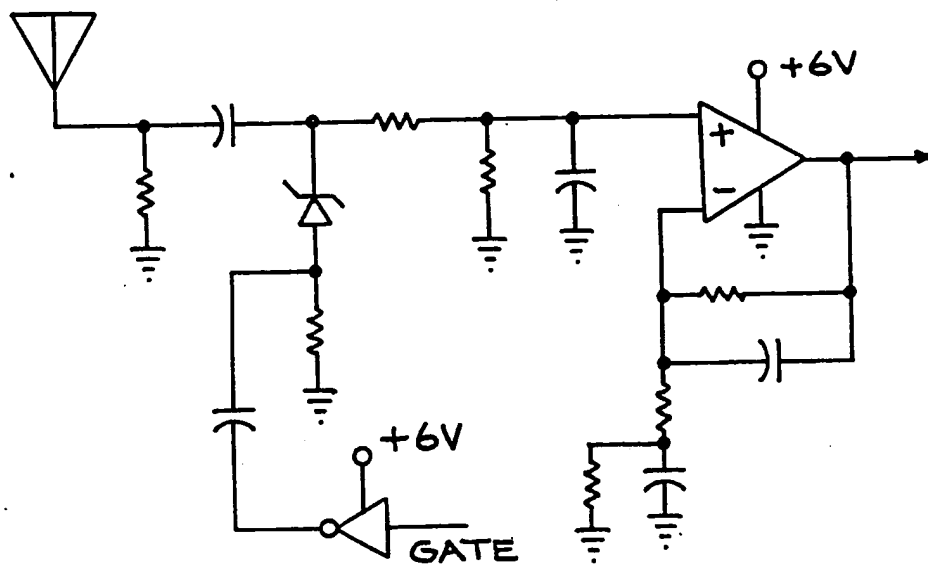
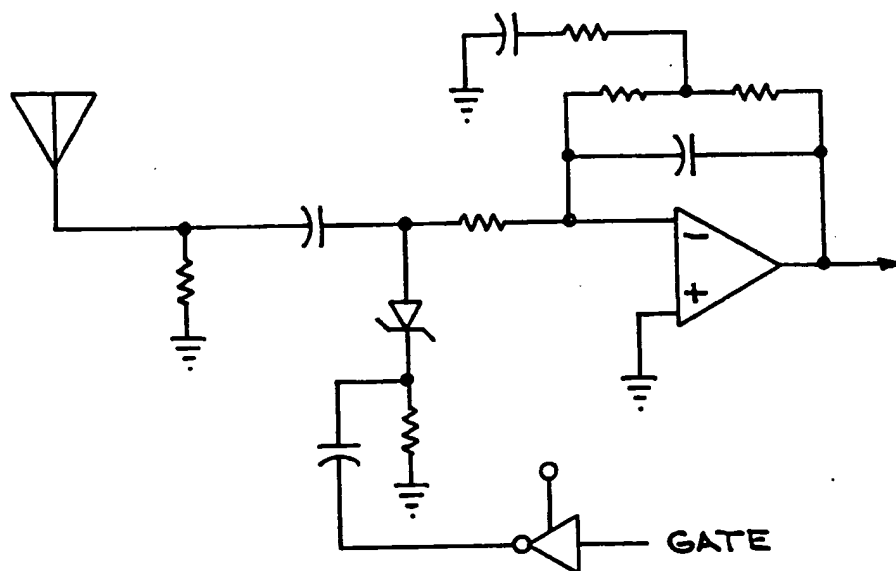


FIG. 7

**FIG. 9A****FIG. 9B**

INTERNATIONAL SEARCH REPORT

Li. national application No.
PCT/US94/03893

A. CLASSIFICATION OF SUBJECT MATTER IPC(5) :H04K 1/00 US CL :375/1 According to International Patent Classification (IPC) or to both national classification and IPC		
B. FIELDS SEARCHED Minimum documentation searched (classification system followed by classification symbols) U.S. : 375/1; 380/34 Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)		
C. DOCUMENTS CONSIDERED TO BE RELEVANT		
Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A,P	US,A, 5,271,034 (ABAUNZA) 14 DECEMBER 1993	1-20
A,P	US, A, 5,267,260 (LEE) 30 NOVEMBER 1993	1-20
A,P	US, A, 5,265,120 (SANDERFORD, JR.) 23 NOVEMBER 1993	1-20
A	US, A, 5,119,396 (SANDERFORD, JR) 02 JUNE 1992	1-20
A	US,A, 5,095,312 (JEHLE ET AL.) 10 MARCH 1992	1-20
<input type="checkbox"/> Further documents are listed in the continuation of Box C. <input type="checkbox"/> See patent family annex.		
* Special categories of cited documents: "A" document defining the general state of the art which is not considered to be part of particular relevance "E" earlier document published on or after the international filing date "L" document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified) "O" document referring to an oral disclosure, use, exhibition or other means "P" document published prior to the international filing date but later than the priority date claimed	"T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention "X" document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone "Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art "&" document member of the same patent family	
Date of the actual completion of the international search 25 MAY 1994		Date of mailing of the international search report JUN 03 1994
Name and mailing address of the ISA/US Commissioner of Patents and Trademarks Box PCT Washington, D.C. 20231 Facsimile No. (703) 305-3230		Authorized officer <i>Drake Godwyn Jr</i> TOD R. SWANN Telephone No. (703) 308-0766